

11046 U.S. PTO
10/001472

11/01/01

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPLICATION

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10001472	11/01/2001	237	436	296	2812

**APPLICANTS: Havemann Robert;

2815

**CONTINUING DATA VERIFIED:

THIS APPLN CLAIMS BENEFIT OF 60/251,494 12/05/2000

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** FOREIGN APPLICATIONS VERIFIED:

PG-PUB DO NOT PUBLISH

RESCIND

Foreign priority claimed

yes no

ATTORNEY DOCKET NO

35 USC 119 conditions met

yes no

TI-27506

Verified and Acknowledged Examiners initials

TITLE : Integrated circuit having a doped porous dielectric and method of manufacturing the same

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due Date Paid

TERMINAL

DISCLAIMER

Assistant Examiner

Primary Examiner

PREPARED FOR ISSUE

CLAIMS ALLOWED

Total Claims

Print Claim for
O.G

DRAWING

Sheets Drwg.

Figs. Drwg.

Print Fig.

Applicant Examiner

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